

Exhibit E



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VIA FEDERAL EXPRESS

Howard G. Pollack
Fish & Richardson P.C.
500 Arguello Street, Suite 500
Redwood City, CA 94036

Re: Power Integrations v. Fairchild Semiconductor et al. (CA 04-1371 JJF)

Dear Howard:

Please see the enclosed CD which contains documents received from third party Intersil. The documents Bates labeled I-000415-420 contain pages from Mr. Beasom's notebook, which has previously been produced. The previous version cut off the bottom of some of the pages, so these pages are being re-produced to ensure that the parties have a complete copy of the notebook. The documents Bates labeled I-000421-451 contain documentation and test data relating to Mr. Beasom's prototype test wafers.

Sincerely,

Brian H. VanderZanden

BHV:ma5

Enclosure

cc: William J. Marsden, Jr.
Frank E. Scherkenbach